

Figure 1

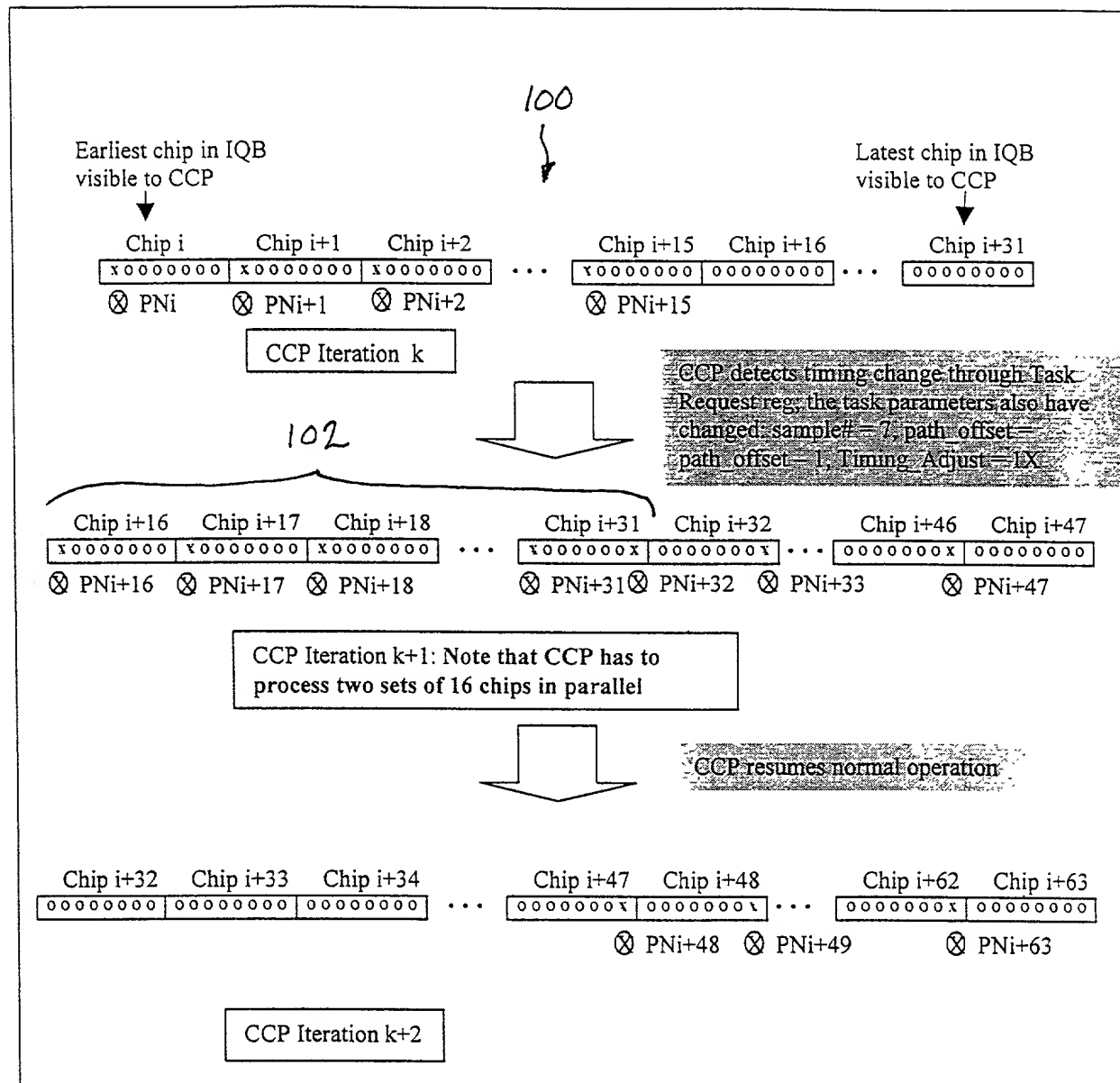


Figure 2

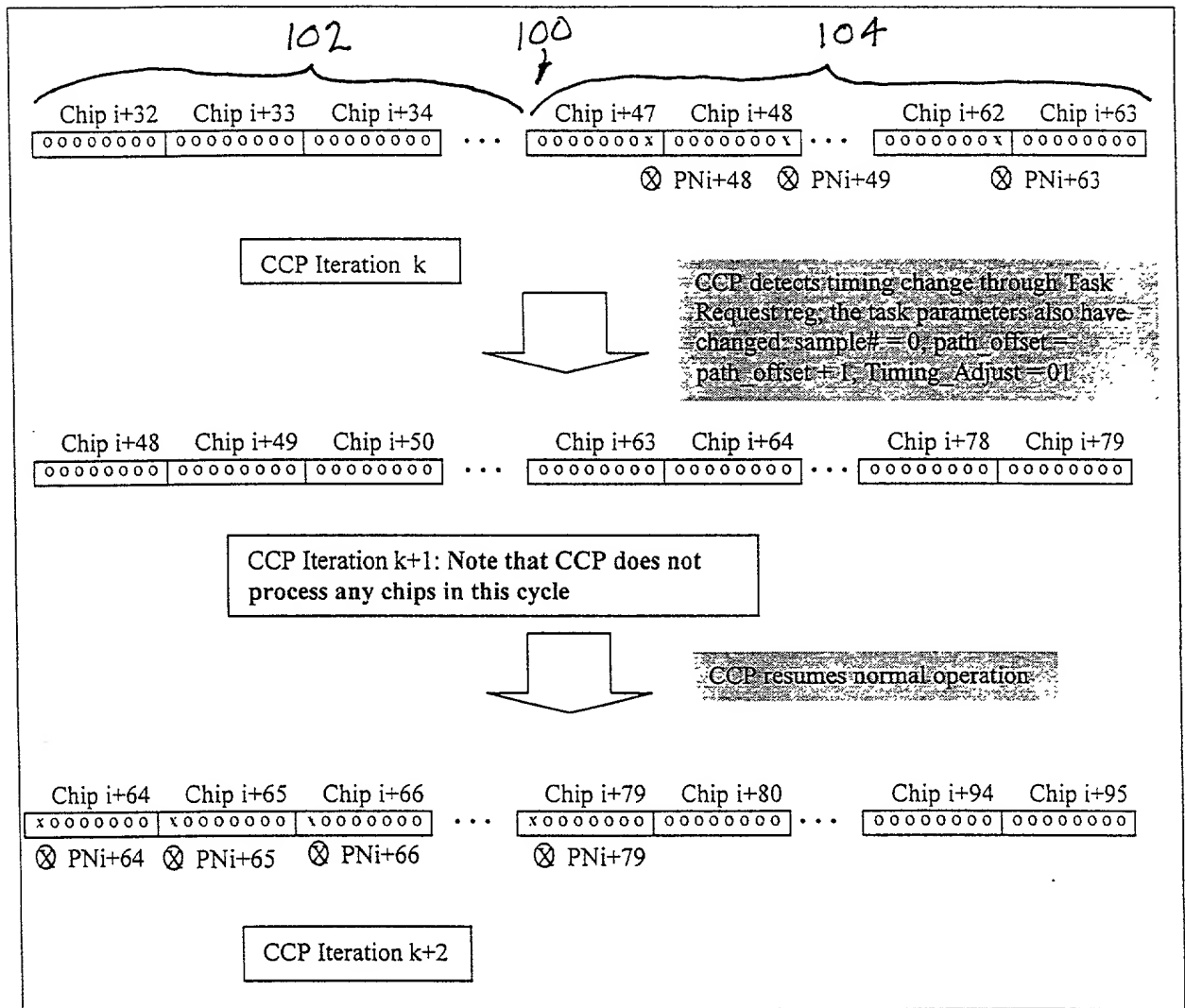


Figure 3

Dll\_delta = 3 (+/- 3/8<sup>th</sup> chip delays for DLL)

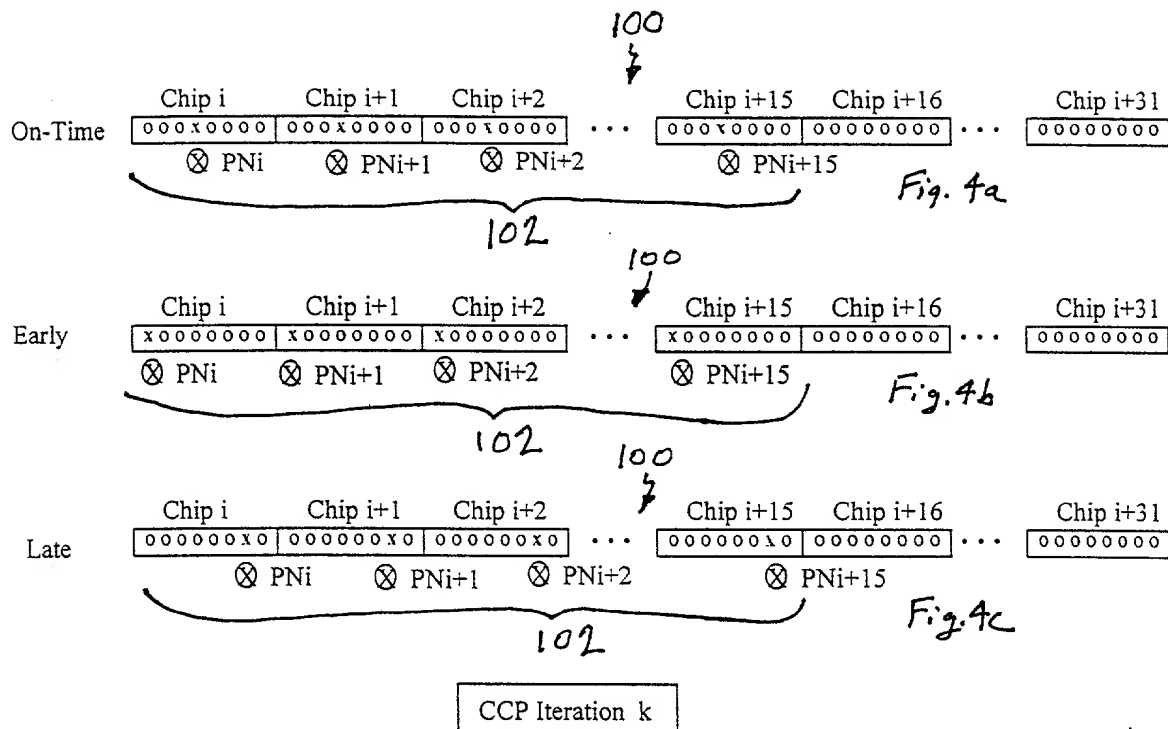


Figure 4

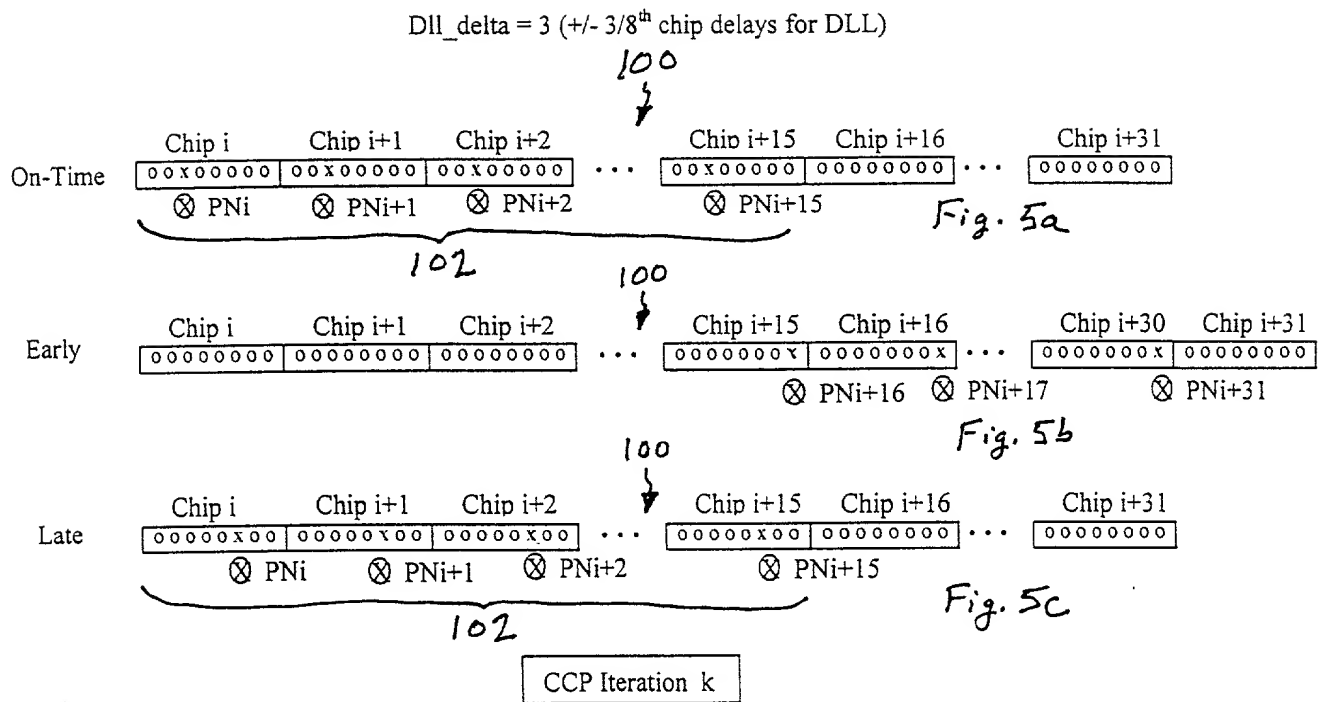


Figure 5

DLL\_delta = 3 (+/- 3/8<sup>th</sup> chip delays for DLL)

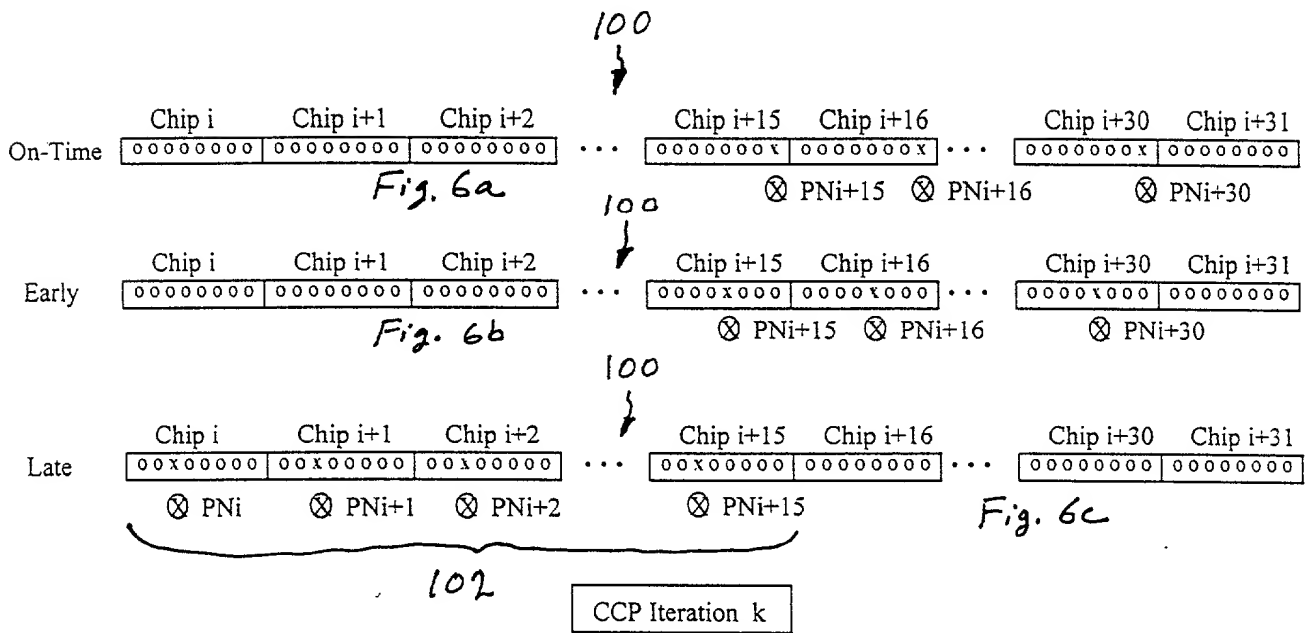


Figure 6

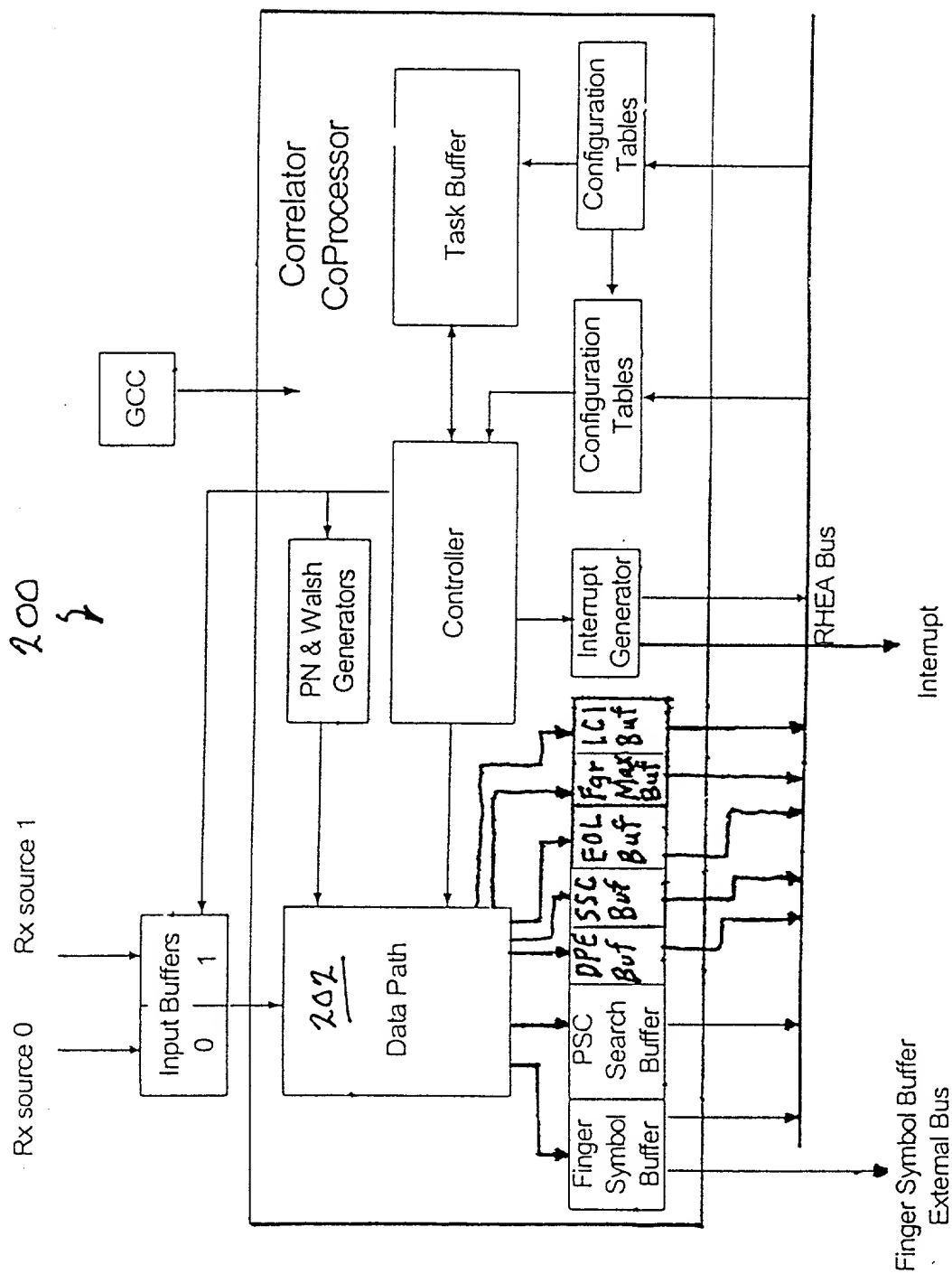


Figure 7 CCP Top-Level Block Diagram

300

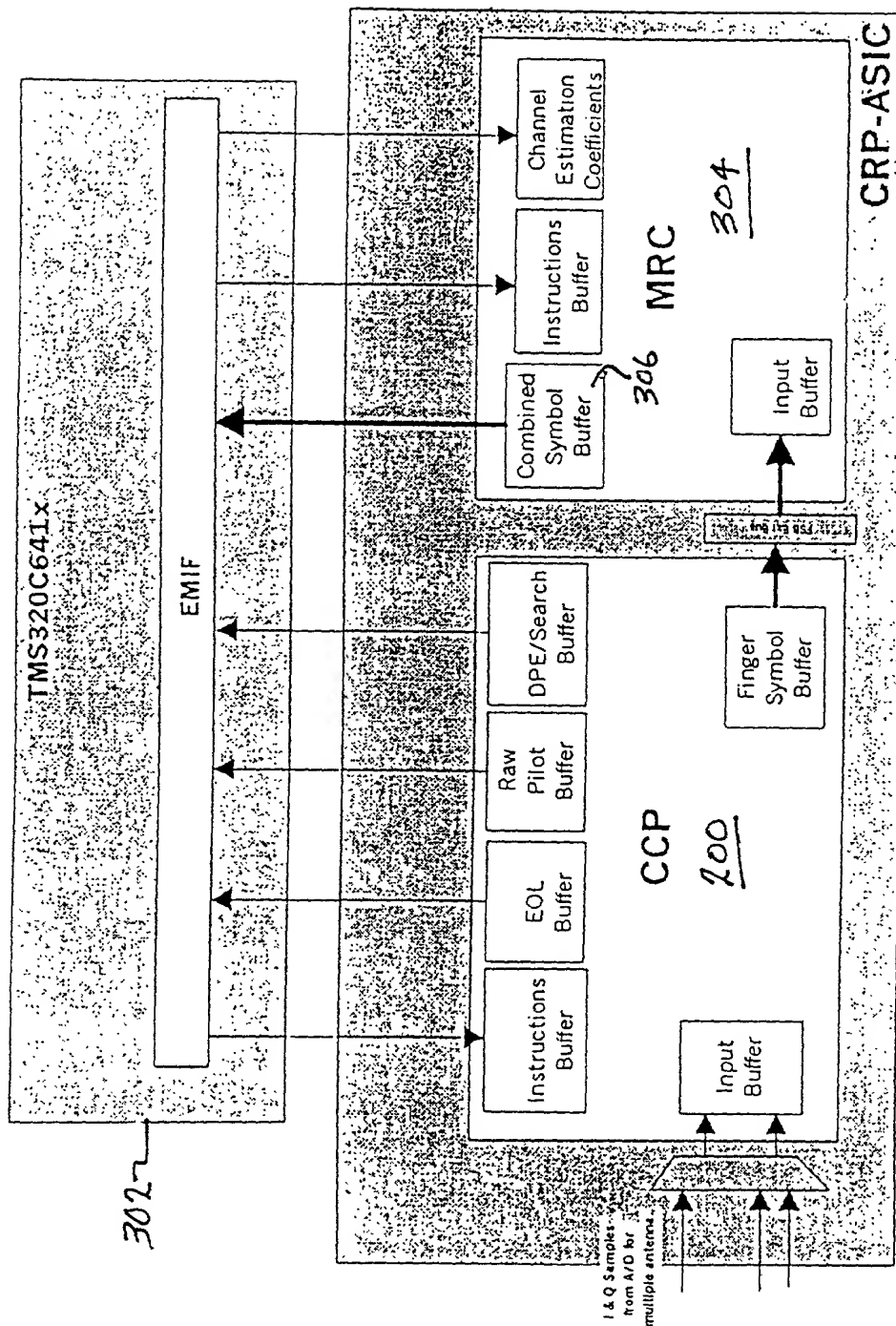


Figure 8